

Troubleshooting Your SLC Processor

Before troubleshooting your SLC 500 system, please obtain an SLC 500 Modular Hardware Style User Manual, publication 1747-UM011. Refer to the chapter on troubleshooting.

In addition to the SLC 500 Modular Hardware Style User Manual, publication 1747-UM011, the SLC 500 Instruction Set Reference Manual, publication 1747-RM001, contains explanations and examples for the entire instruction set as well as for all status words and bits. It also contains explanations for all possible fault codes found in status word S:6.

Specifications

SLC 5/03, SLC 5/04, and SLC 5/05 Processors

Attribute	SLC 5/03			SLC 5/04			SLC 5/05		
	L531	L532	L533	L541	L542	L543	L551	L552	L553
Memory (words)	8 K	16 K	32 K	16 K	32 K	64 K	16 K	32 K	64 K
I/O capacity, max	4096 discrete inputs/4096 discrete outputs								
Local system, max	3 chassis / 30 slots								
Programming instructions	107								
Typical scan time ⁽¹⁾	1 ms/K			0.9 ms/K					
Bit execution (XIC)	0.44 μ s			0.37 μ s					
Programming software	SLC 5/03s and SLC 5/04s: RSLogix 500, SLC 5/05s: RSLogix 500								

⁽¹⁾ The scan times are typical for a 1 K ladder logic program consisting of simple ladder logic and communication servicing. Actual scan times depend on your program size, instructions used, and the communication protocol.